

Circuit Simulation Project

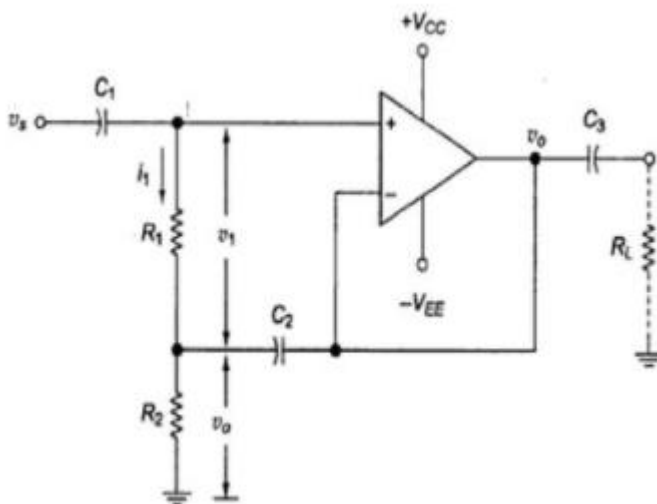
<https://esim.fossee.in/circuit-simulation-project>

Name of the participant : ABDUL SAMAD

Title of the circuit : Simulation of high impedance capacitor coupled Voltage Follower circuit

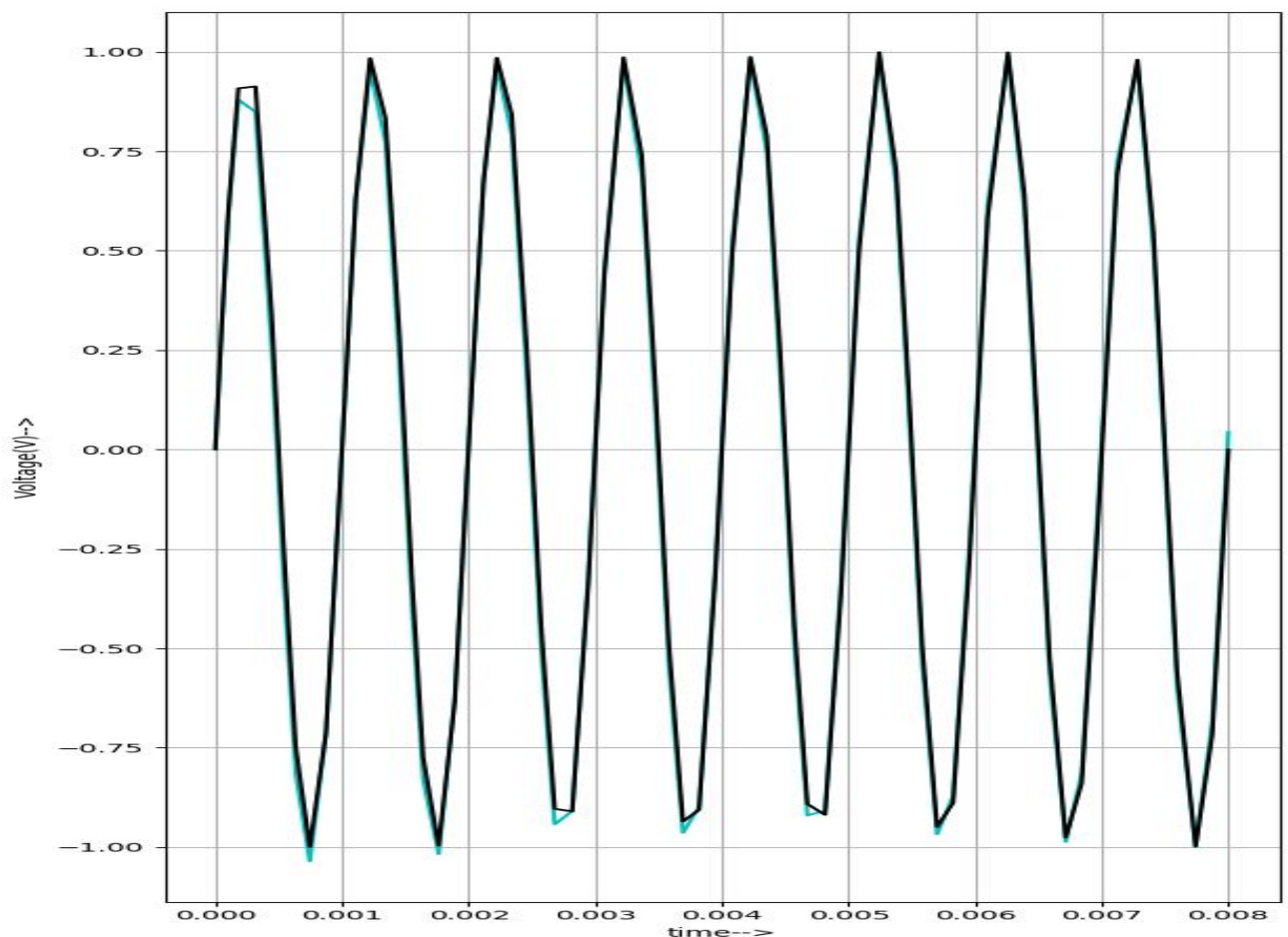
Theory/Description : This circuit buffers AC signals while blocking DC offsets using an input capacitor. The op-amp voltage follower provides high input impedance (protects weak sources like sensors) and low output impedance (drives loads). A resistor to ground stabilizes DC bias, and the RC pair forms a high-pass filter, setting a lower frequency cutoff. Ideal for isolating AC signals (audio, sensors) without loading the source or distorting the output.

Circuit Diagram(s) :



Results (Input, Output waveforms and/or Multimeter readings) :

The circuit outputs a clean, amplified AC signal matching the input waveform *without DC offset*, preserved at unity gain. It suppresses low frequencies below the RC high-pass cutoff (e.g., <2 Hz)



Source/Reference(s) :

<https://www.eeeguide.com/voltage-follower-circuit-diagram/>

[Voltage Follower Circuit Diagram | Capacitor-Coupled Voltage Follower \(eeeguide.com\)](#)

[Subject Name: LINEAR IC's AND APPLICATIONS Subject Code:10EC46 Prepared By: Aparna.P](#)

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Slides by Aparna P.