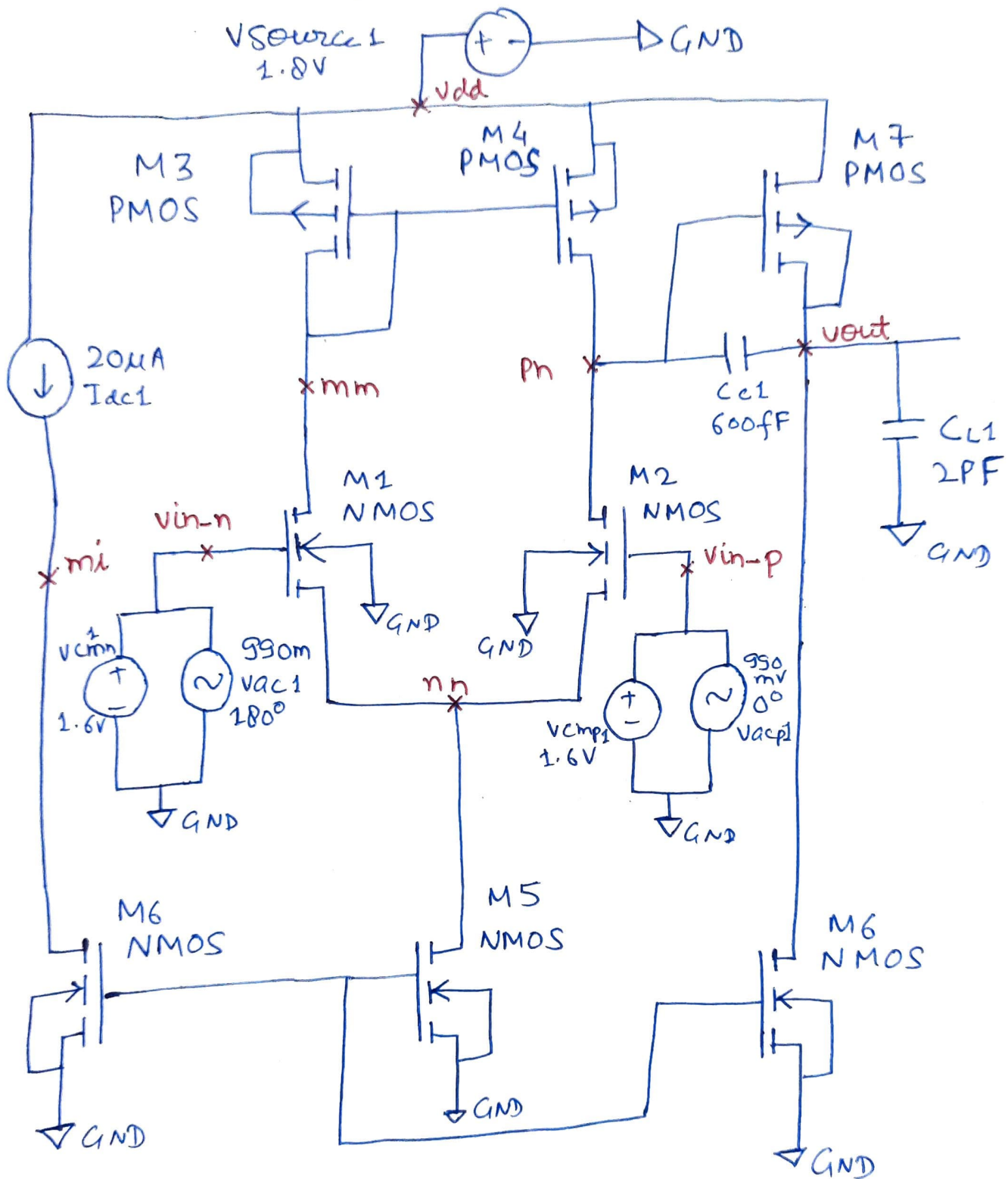


Reference Circuit Diagram of Two stage CMOS Operational Amplifier with all reference nodes \Rightarrow



$x \rightarrow$ Different nodes

GND is also one of the nodes (universal node)