

IMPLEMENTATION OF AND GATE USING NAND GATE:

Theory:

Schematic Diagram:

The below is the schematic diagram of a AND gate built using NAND gate.

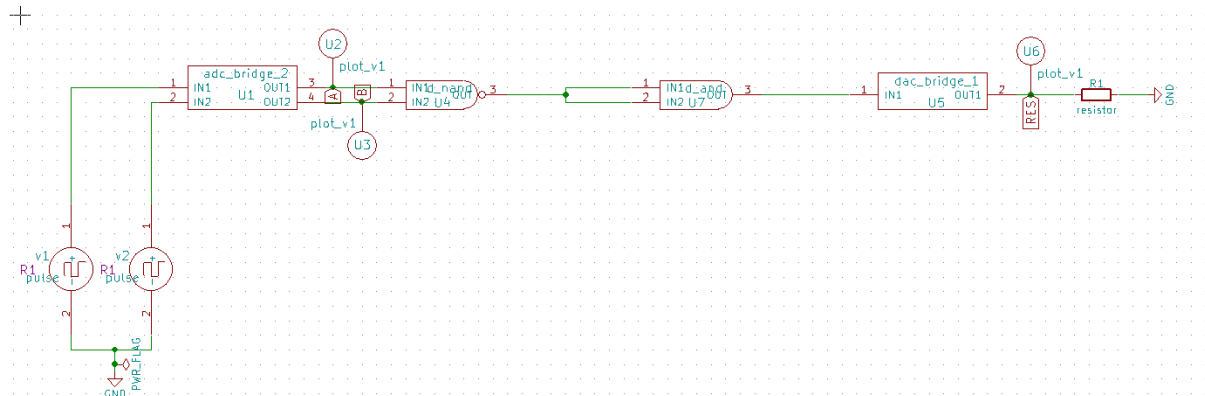


Figure 1: Circuit Diagram

Simulation Result

Ng-spice Simulation:

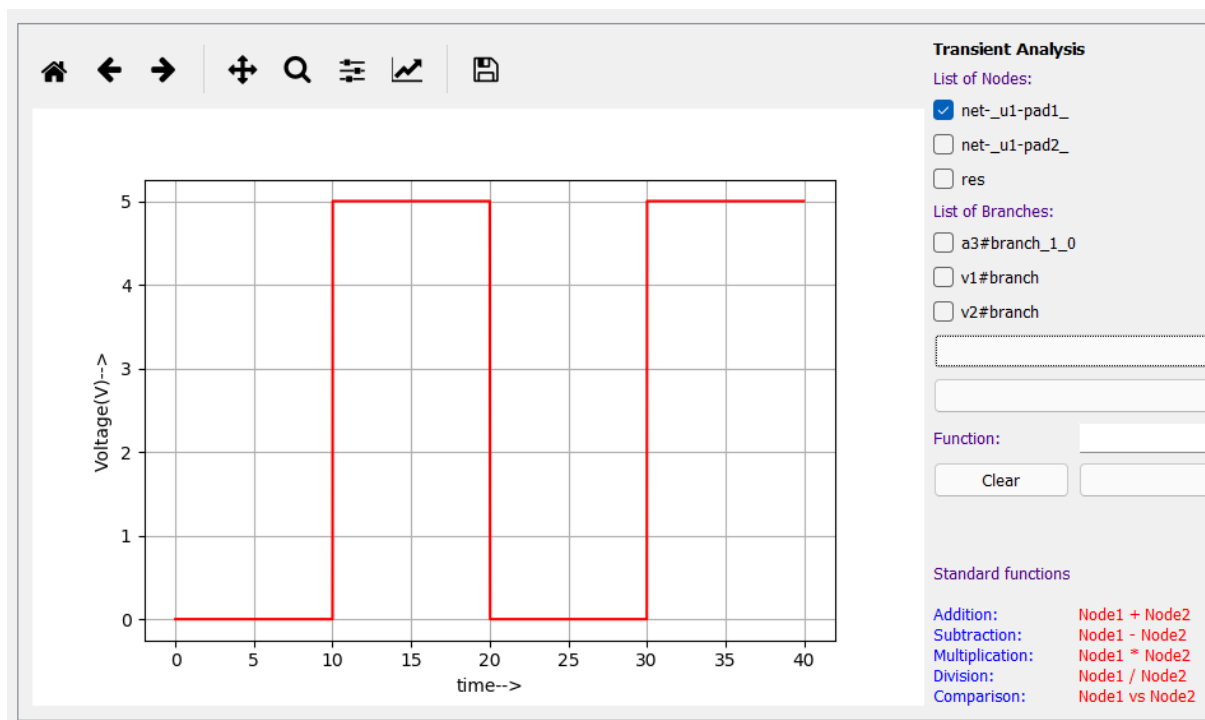


Figure 2: Voltage Source A

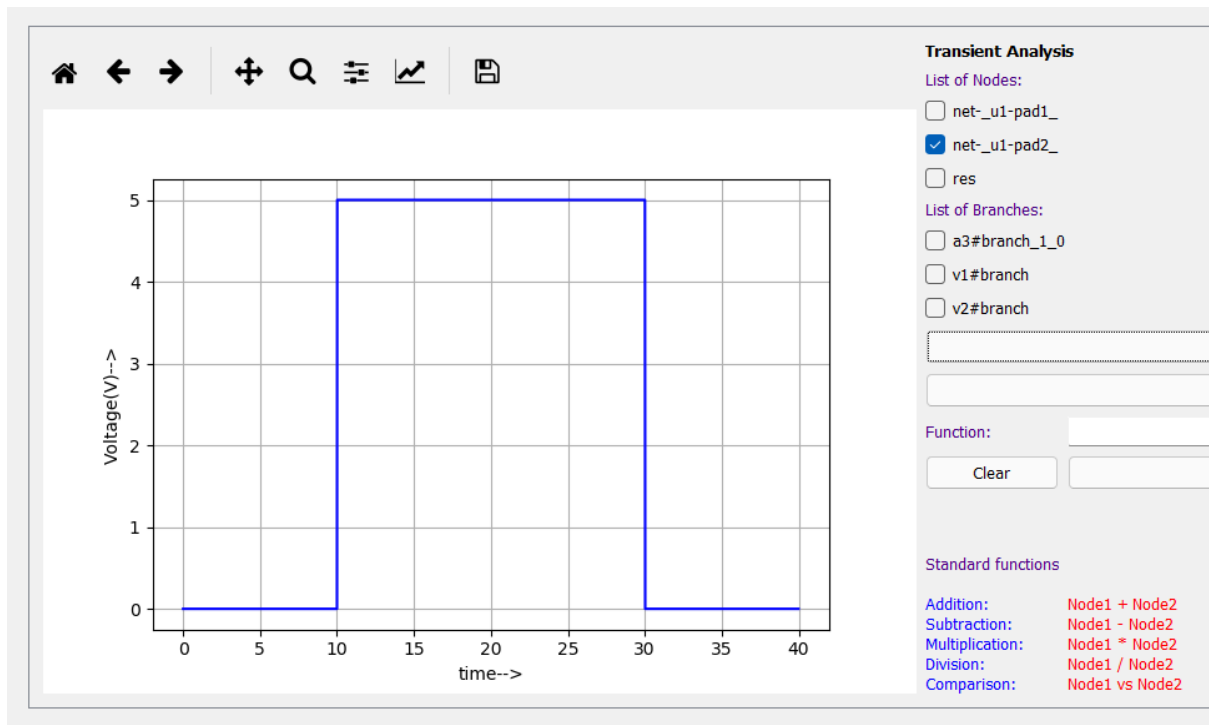


Figure 3: Voltage Source B

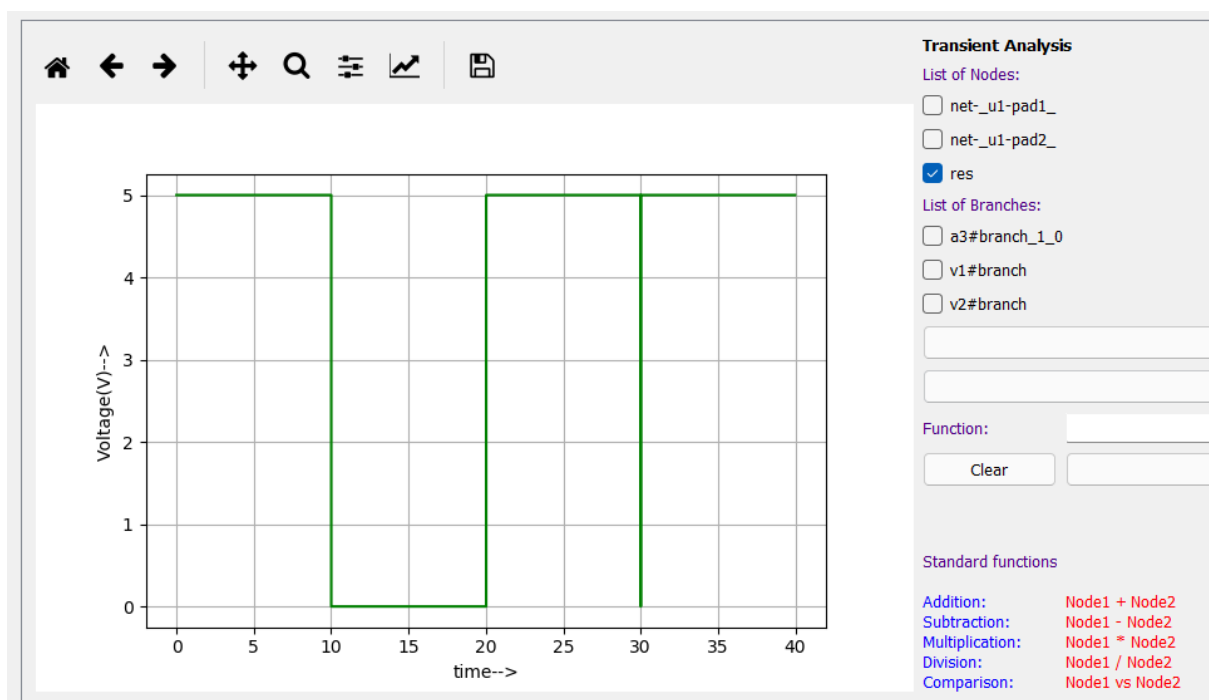


Figure 4: Result

Reference:

<http://www.learningaboutelectronics.com/Articles/AND-gate-from-NAND-gates-circuit.php>