

# IMPLEMENTATION OF OR GATE USING NOR GATE

## Schematic Diagram:

The below is the schematic diagram of a OR gate built using NOR gate.

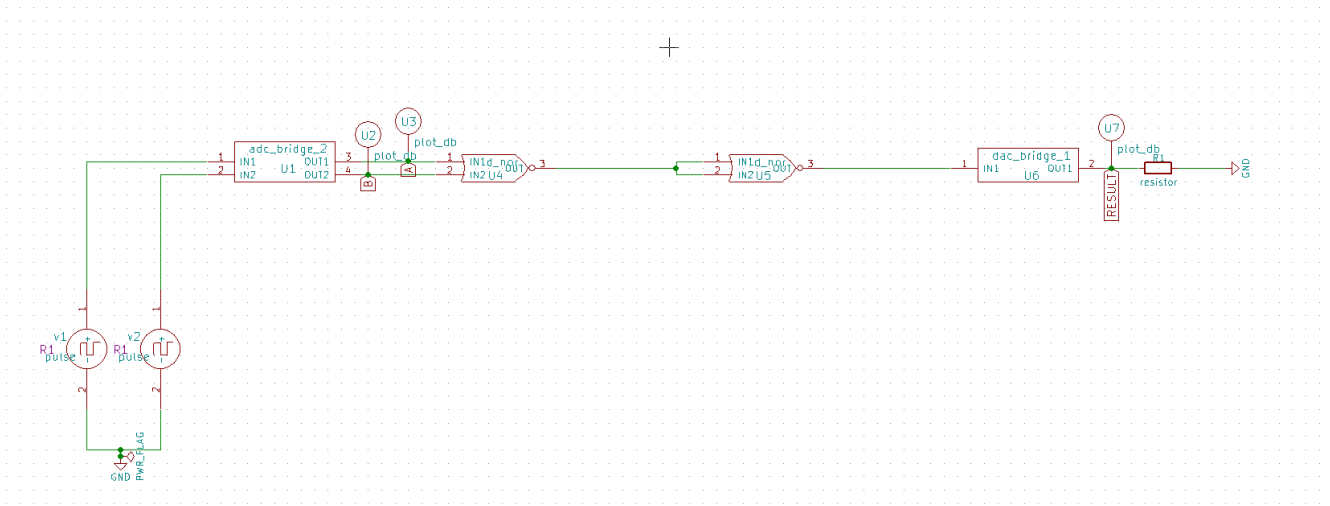


Figure 1: Circuit Diagram

## Ng-spice Simulation:

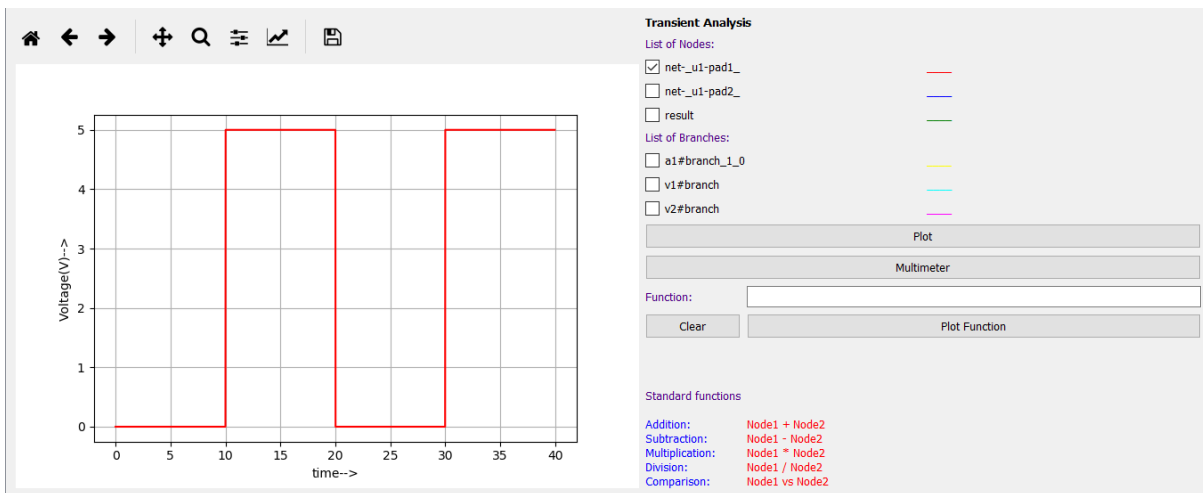


Figure 2: Voltage source A

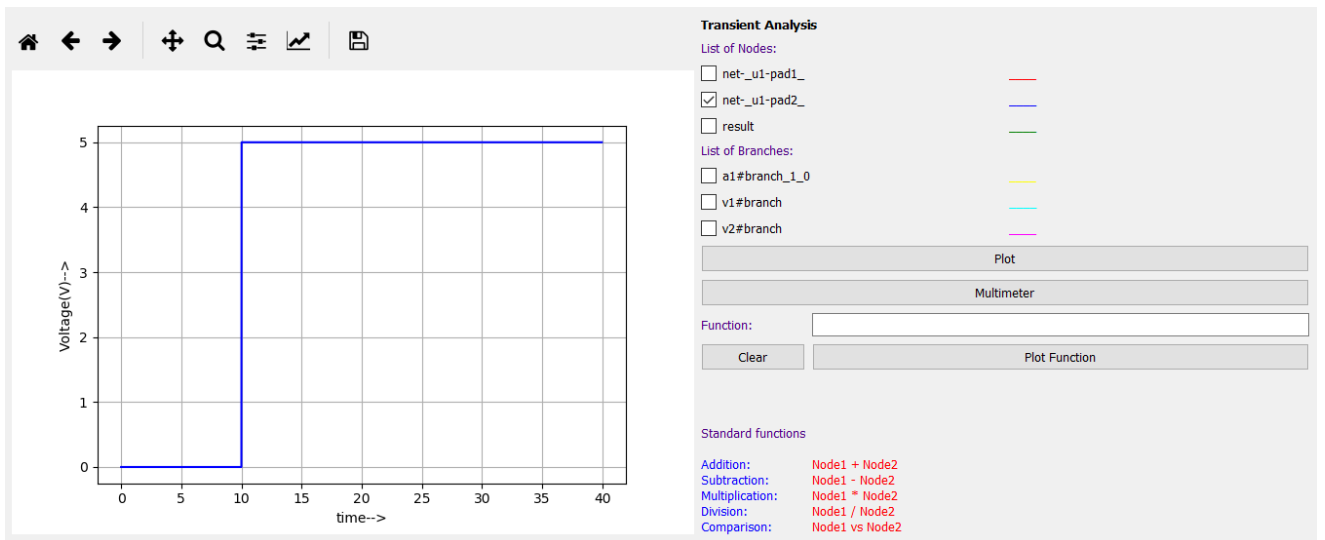


Figure 3: Voltage source B

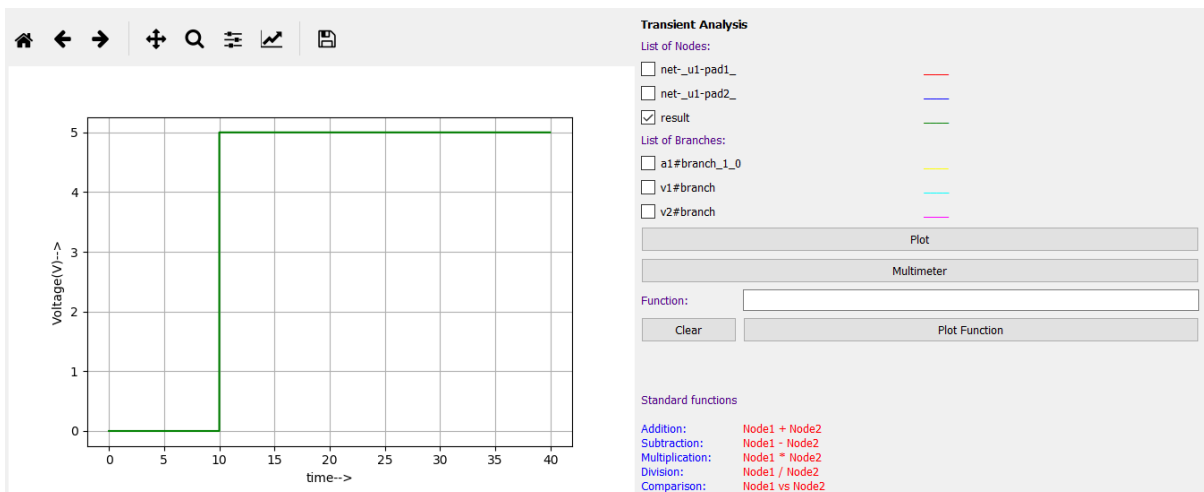


Figure 4: Result

**Reference:**

[https://www.iitr.ac.in/departments/PH/uploads/Teaching%20Laboratory/Electronics/5.1  
 nterconversion%20of%20Universal%20Gates%20and%20De%20Morgans%20Theorem.pdf](https://www.iitr.ac.in/departments/PH/uploads/Teaching%20Laboratory/Electronics/5.1%20interconversion%20of%20Universal%20Gates%20and%20De%20Morgans%20Theorem.pdf)