

Traffic controller design.

Anandita, National Institute of Technology Patna

February 28,2022

Abstract - This report presents a traffic controller for a T-intersection using Verilog and corresponding waveform is achieved as per the state diagram of problem statement.

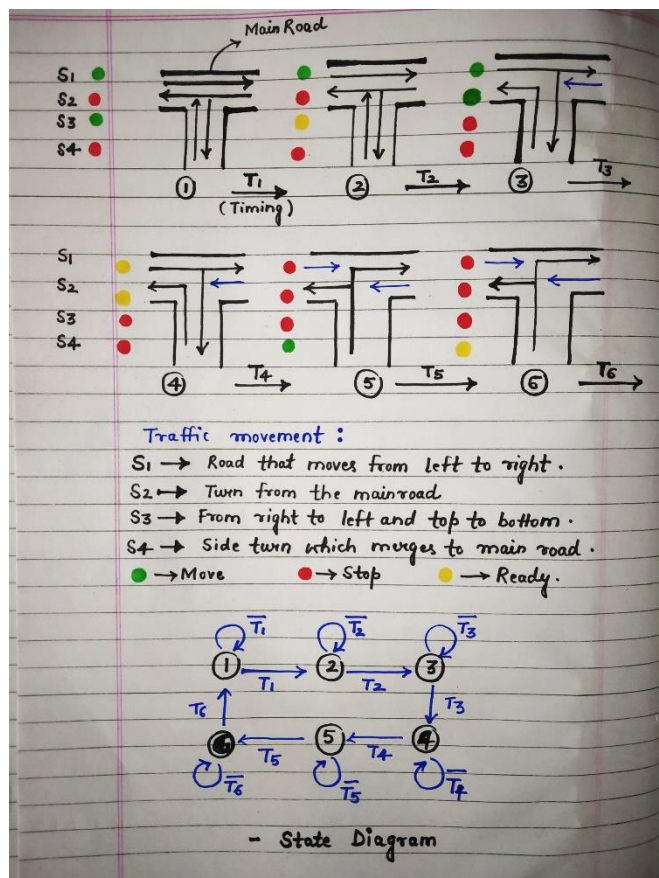
1. INTRODUCTION

Due to the increasing number of commuters on roads, monitoring and control of traffic flow are one of the problems that countries are facing nowadays. This is due to the increasing number of commuters on roads. To address the issue this report presents a traffic controller design for a T intersection.

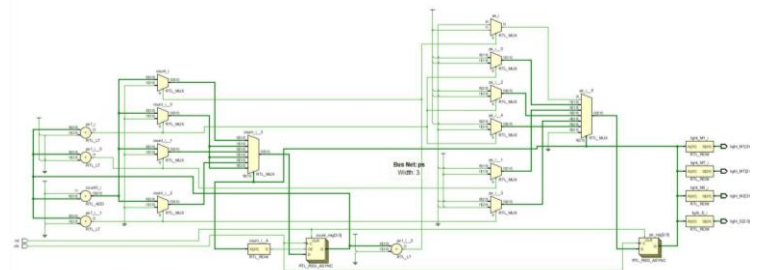
2. REFERENCE CIRCUIT DETAILS

Problem Statement-

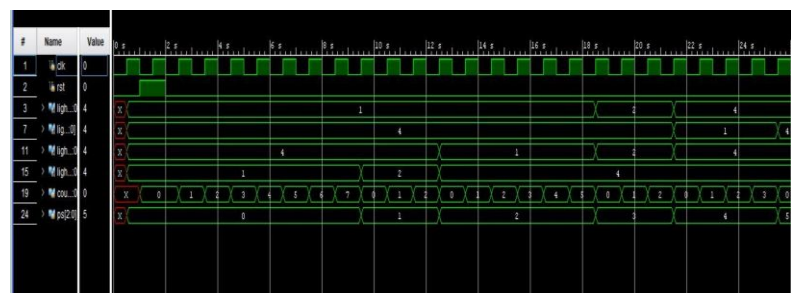
Traffic controller design for a T-Intersection where we have 6 different states conditions and corresponding lights (Red, Green, Yellow) status explained below.



3. REFERENCE CIRCUIT- RTL Schematic



4. REFERENCE WAVEFORMS



5. REFERENCES

- [1] NPTEL Lecture on Digital Design by Professor Srinivasan.